

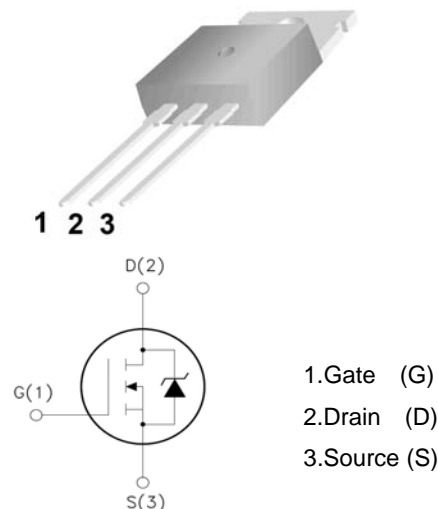
XXW160N06

60V N-Channel MOSFET

Features:

- Low Intrinsic Capacitances.
- Excellent Switching Characteristics.
- Extended Safe Operating Area.
- Unrivalled Gate Charge : $Q_g = 152\text{nC}$ (Typ.).
- $BVDSS=60\text{V}, I_D=160\text{A}$
- $R_{DS(on)} : 4.0\text{m}\Omega$ (Max) @ $V_G=10\text{V}$
- 100% Avalanche Tested

TO-220



Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Maximum	Unit
V_{DSS}	Drain-to-Source Voltage	60	V
V_{GSS}	Gate-to-Source Voltage	± 20	V
I_D^3	Continuous Drain Current	$T_C=25^\circ\text{C}$	160
		$T_C=100^\circ\text{C}$	105
I_{DP}^4	Pulsed Drain Current	$T_C=25^\circ\text{C}$	600
I_{AS}^5	Avalanche Current	28	A
E_{AS}^5	Avalanche energy	900	
PD	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	200
		$T_C=100^\circ\text{C}$	92.5
T_J, T_{STG}	Junction & Storage Temperature Range	-55~175	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta jc}$	Thermal Resistance-Junction to Case	0.68	$^\circ\text{C/W}$
$R_{\theta ja}$	Thermal Resistance-Junction to Ambient	62.5	

Electrical Characteristics (TA=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	60	—	—	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =60V, V _{GS} =0V	—	—	1	uA
		T _J =125°C	—	—	100	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	2	3	4	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	—	—	±100	nA
R _{DS(on)} ¹	Drain-Source On-Resistance	V _{GS} =10V, I _D =80A	—	2.9	4.0	mΩ
			—	—	—	
Diode Characteristics						
V _{SD} ¹	Diode Forward Voltage	I _{SD} =80A, V _{GS} =0V	—	0.88	1.2	V
I _S ³	Diode Continuous Forward Current		—	—	160	A
t _{rr}	Reverse Recovery Time	I _F =80A, T _J =25°C	—	73	—	nS
Q _{rr}	Reverse Recovery Charge	di/dt=100A/us	—	98	—	nC
Dynamic Characteristics²						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, Frequency=1MHz	—	0.7	—	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =30V Frequency=1MHz	—	7820	—	pF
C _{oss}	Output Capacitance		—	634	—	
C _{rss}	Reverse Transfer Capacitance		—	502	—	
t _{d(on)}	Turn-On Delay Time	V _{DD} =30V, RL=0.4Ω V _{GS} =10V, R _G =2.5Ω	—	31	—	nS
t _r	Rise Time		—	29	—	
t _{d(off)}	Turn-Off Delay Time		—	110	—	
t _f	Fall Time		—	46	—	
Gate Charge Characteristics²						
Q _g	Total Gate Charge	V _{DS} =30V, V _{GS} =10V I _D =160A	—	152	—	nC
Q _{gs}	Gate-to-Source Charge		—	33	—	
Q _{gd}	Gate-to-Drain Charge		—	55	—	

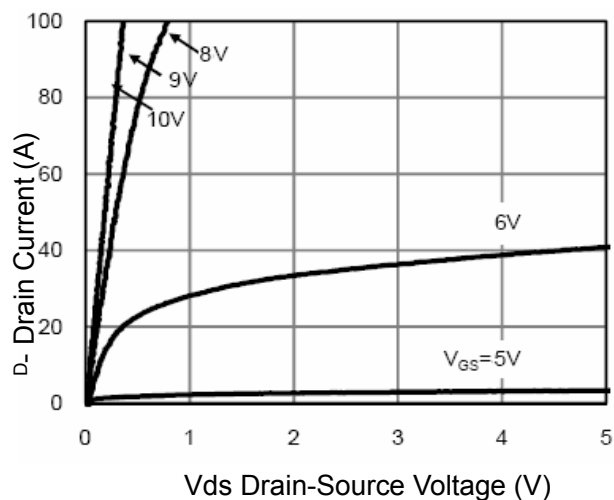
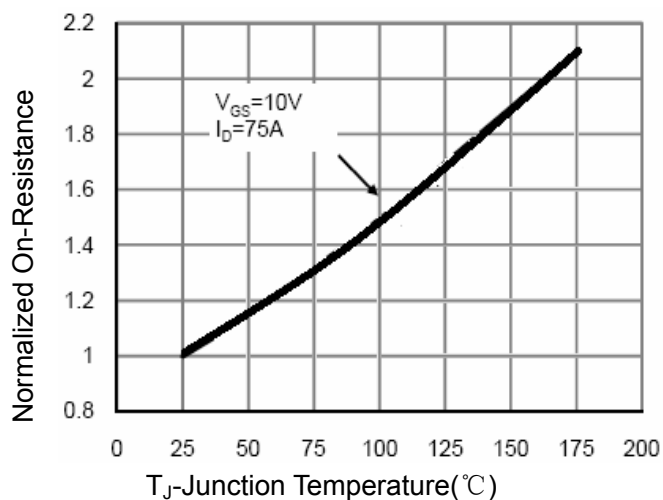
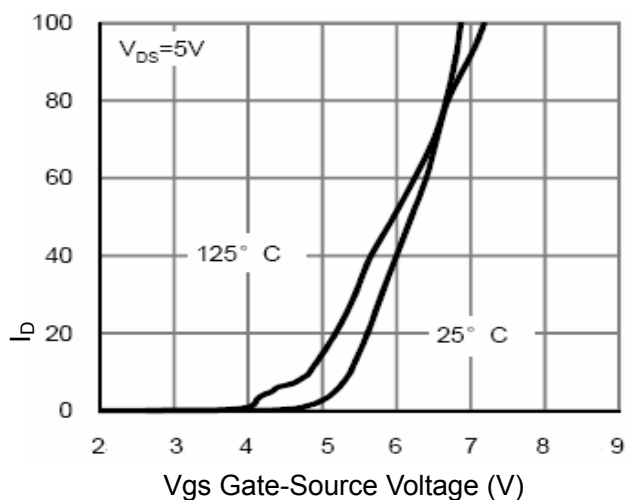
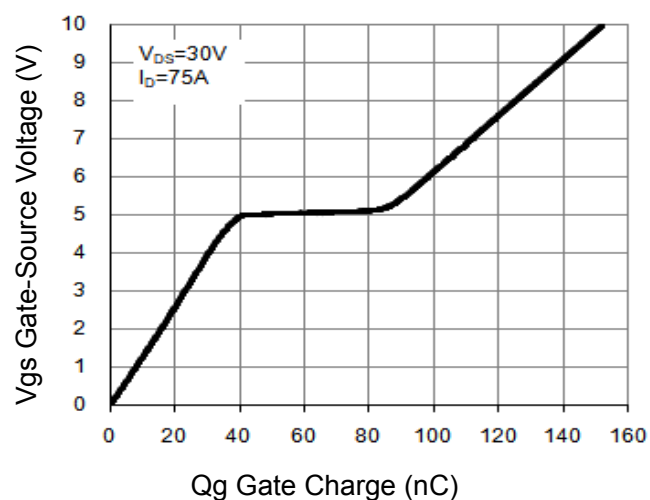
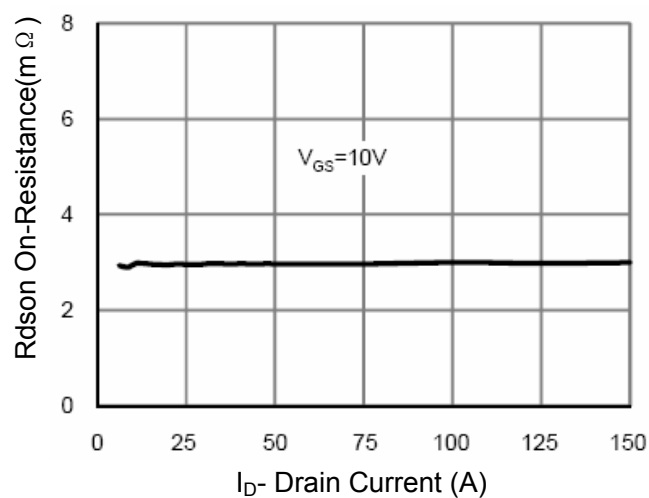
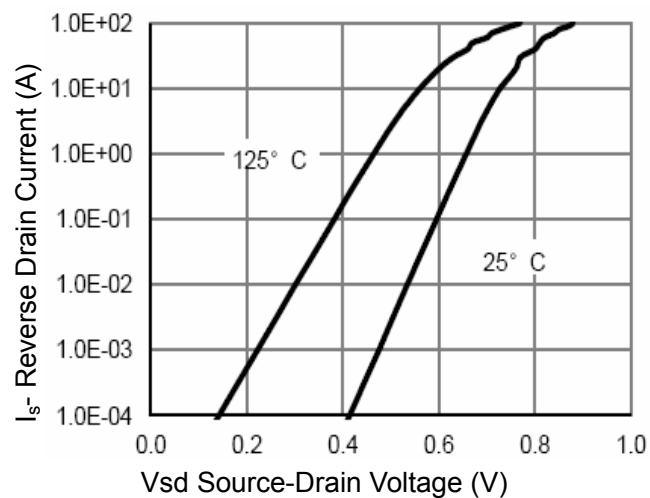
Note: 1: Pulse test; pulse width ≤ 300us, duty cycle ≤ 2%.

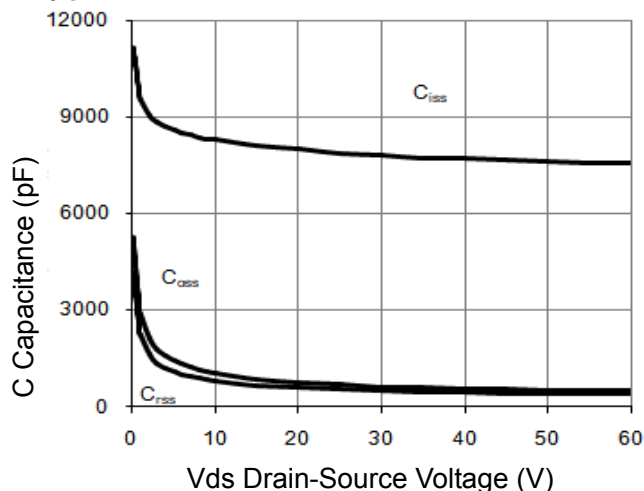
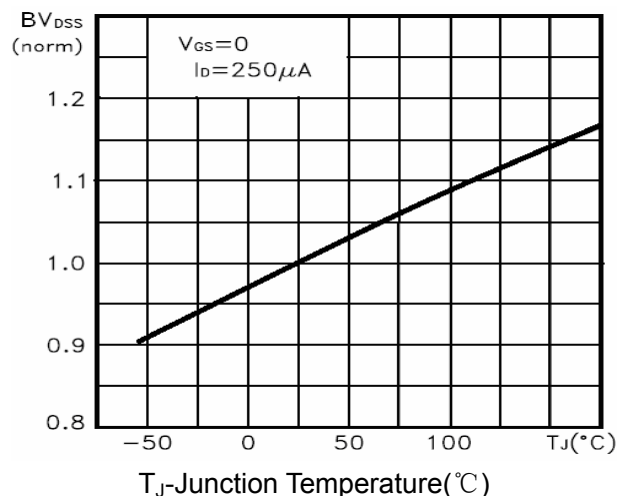
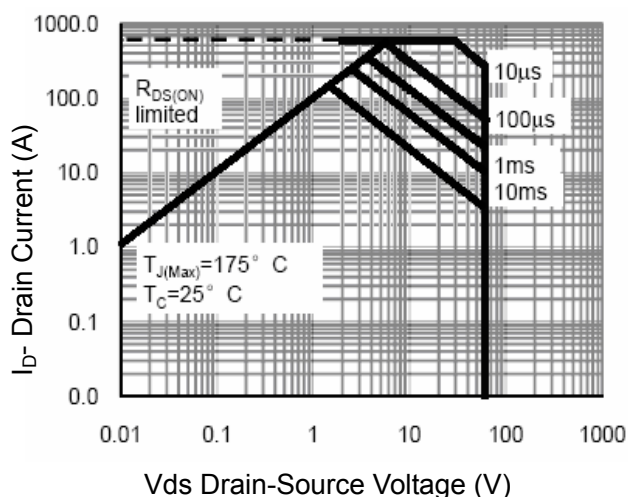
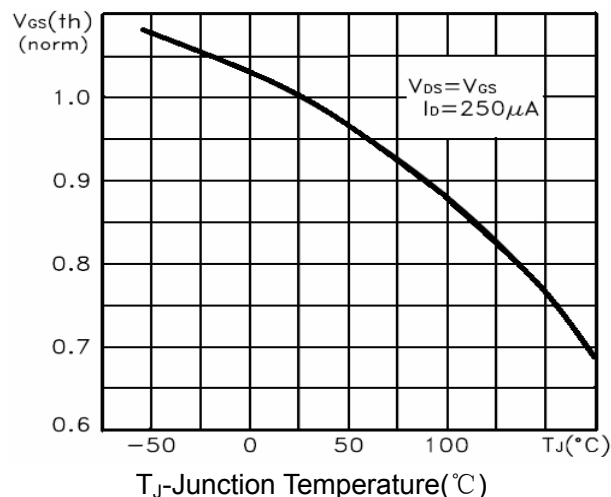
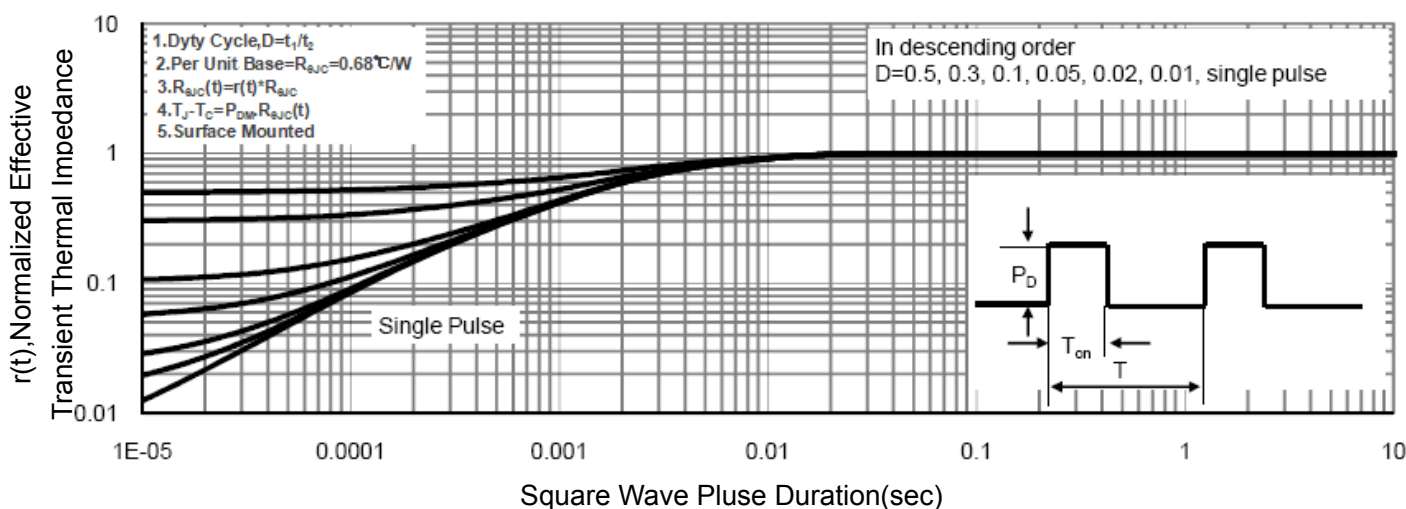
2: Guaranteed by design, not subject to production testing.

3: Package limitation current is 75A. Calculated continuous current based on maximum allowable junction temperature.

4: Repetitive rating, pulse width limited by max junction temperature.

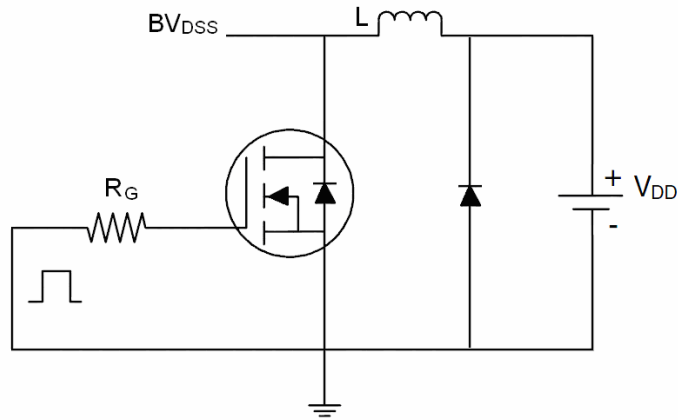
5: Starting T_J = 25°C, L = 0.5mH

Typical Characteristics

Figure 1 Output Characteristics

Figure 4 Rdson-Junction Temperature

Figure 2 Transfer Characteristics

Figure 5 Gate Charge

Figure 3 Rdson- Drain Current

Figure 6 Source- Drain Diode Forward

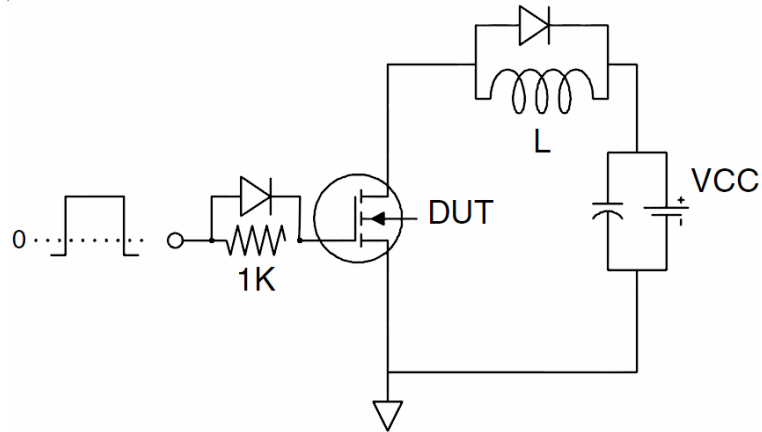
Typical Characteristics

Figure 7 Capacitance vs Vds

Figure 9 BV_{DSS} vs Junction Temperature

Figure 8 Safe Operation Area

Figure 10 $V_{GS(th)}$ vs Junction Temperature

Figure 11 Normalized Maximum Transient Thermal Impedance

Test circuit

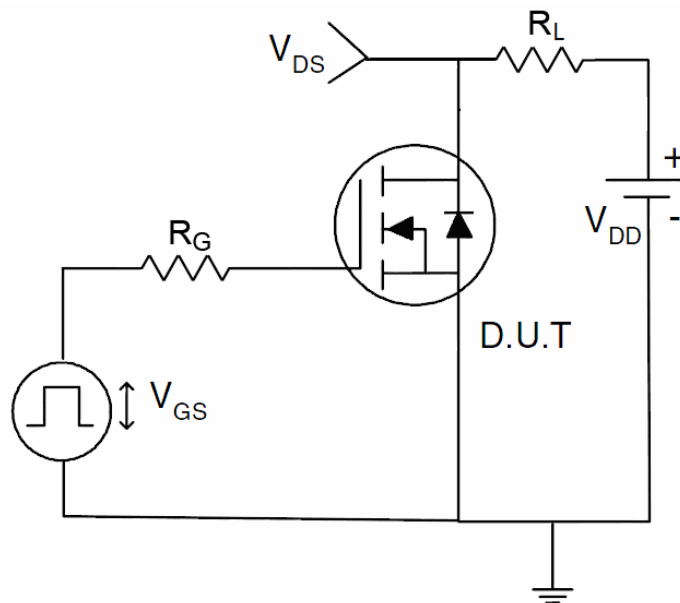
1) E_{AS} test Circuits



2) Gate charge test Circuit:

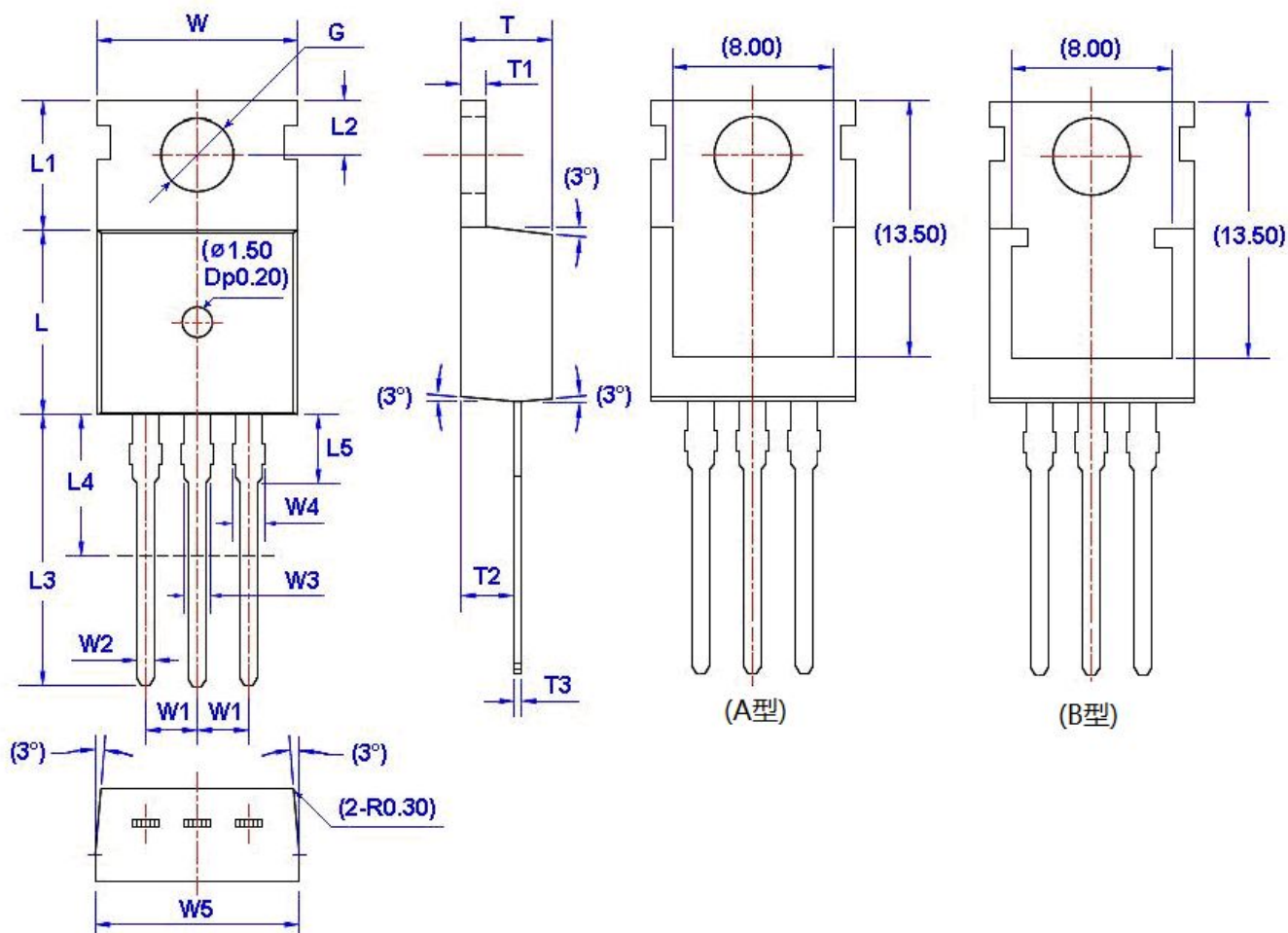


3) Switch Time Test Circuit:



Package Dimension
TO-220

Unit:mm



Symbol	Size		Symbol	Size		Symbol	Size		Symbol	Size	
	Min	Max		Min	Max		Min	Max		Min	Max
W	9.66	10.28	W5	9.80	10.20	L4**	6.20	6.60	T3	0.45	0.60
W1	2.54 (TYP)		L	9.00	9.40	L5	2.79	3.30	G(Φ)	3.50	3.70
W2	0.70	0.95	L1	6.40	6.80	T	4.30	4.70			
W3	1.17	1.37	L2	2.70	2.90	T1	1.15	1.40			
W4*	1.32	1.72	L3	12.70	14.27	T2	2.20	2.60			