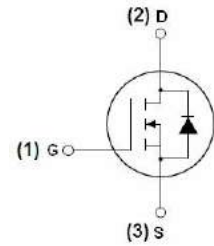


80V N-Channel Enhancement Mode MOSFET

Description

The 85N08 uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge .

The complementary MOSFETs may be used to form a level shifted high side switch, and for a host of other



application

General Features

$V_{DS} = 80V, I_D = 85A$

$R_{DS(ON)} < 7.2m @ V_{GS} = 10V$

Application

- Power switching application
- Hard Switched and High Frequency Circuits
- Uninterruptible Power Supply



Absolute Maximum Ratings@ $T_J = 25^\circ C$ (unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	80	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_c = 25^\circ C$	Drain Current, $V_{GS} @ 10V^3$	85	A
$I_D @ T_c = 100^\circ C$	Drain Current, $V_{GS} @ 10V$	58	A
IDM	Pulsed Drain Current ¹	300	A
$P_D @ T_c = 25^\circ C$	Total Power Dissipation	125	W
$P_D @ T_A = 25^\circ C$	Total Power Dissipation ⁴	2.4	W
TSTG	Storage Temperature Range	-55 to 175	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 175	$^\circ C$
R_{thj-c}	Maximum Thermal Resistance, Junction-case	1.2	$^\circ C/W$
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient (PCB mount) ⁴	62.5	$^\circ C/W$
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient	110	$^\circ C/W$

80V N-Channel Enhancement Mode MOSFET

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
B _V DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	80	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =40A	-	-	7.2	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	2	-	5	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =40A	-	55	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =60V, V _{GS} =0V	-	-	25	uA
I _{GSS}	Gate-Source Leakage	V _{GS} =+20V, V _{DS} =0V	-	-	+100	nA
Q _g	Total Gate Charge ²	I _D =40A	-	58	92	nC
Q _{gs}	Gate-Source Charge	V _{DS} =60V	-	14	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =10V	-	29	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DS} =40V	-	13	-	ns
t _r	Rise Time	I _D =40A	-	80	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =1Ω	-	26	-	ns
t _f	Fall Time	V _{GS} =10V	-	12	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	2350	3760	pF
C _{oss}	Output Capacitance	V _{DS} =25V f=1.0MHz	-	390	-	pF
C _{rss}	Reverse Transfer Capacitance		-	245	-	pF
R _g	Gate Resistance	f=1.0MHz	-	1.3	-	Ω
V _{SD}	Forward On Voltage ²	I _S =40A, V _{GS} =0V	-	-	1.3	V
t _{rr}	Reverse Recovery Time ²	I _S =10A, V _{GS} =0V, dI/dt=100A/μs	-	46	-	ns
Q _{rr}	Reverse Recovery Charge		-	83	-	nC

Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Package limitation current is 75A.
- 4.Surface mounted on 1 in² copper pad of FR4 board

80V N-Channel Enhancement Mode MOSFET

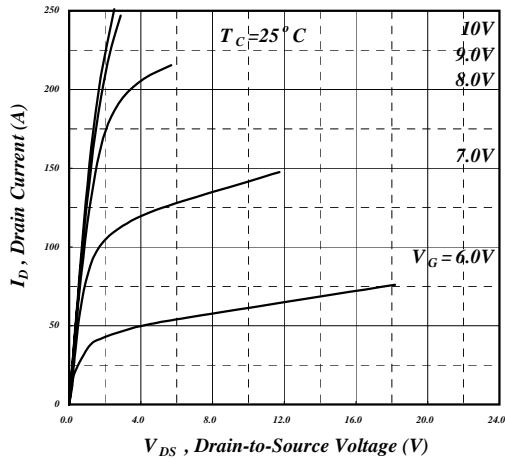


Fig 1. Typical Output Characteristics

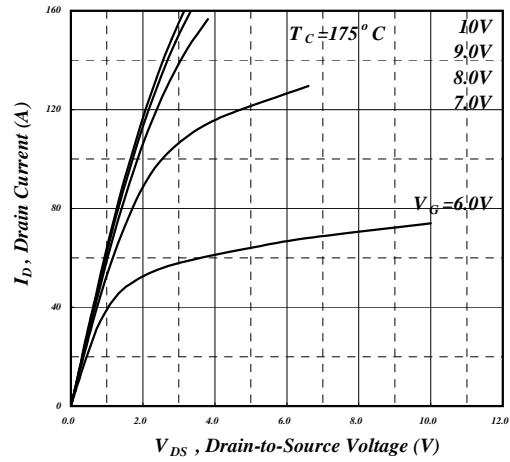


Fig 2. Typical Output Characteristics

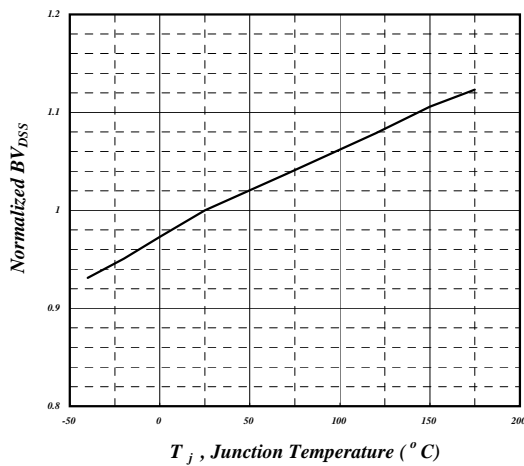


Fig 3. Normalized BV_{DSS} v.s. Junction Temperature

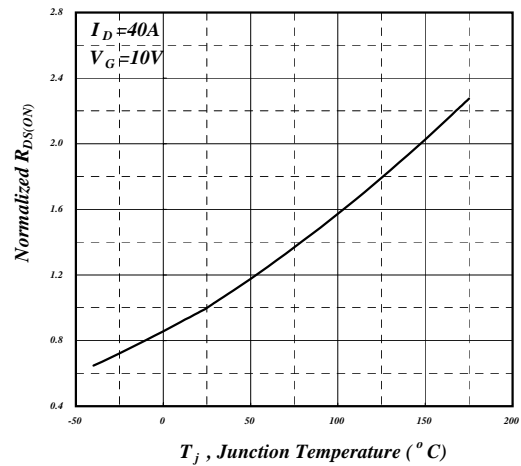


Fig 4. Normalized On-Resistance v.s. Junction Temperature

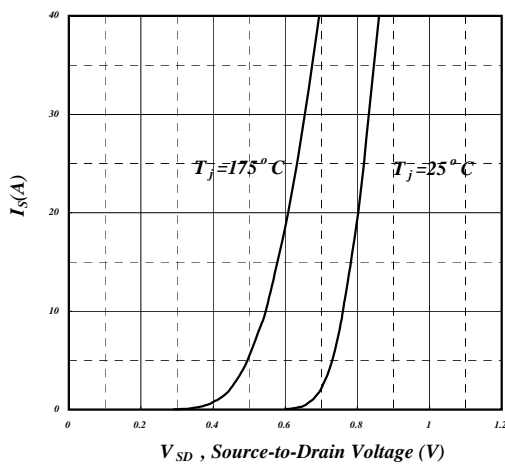


Fig 5. Forward Characteristic of Reverse Diode

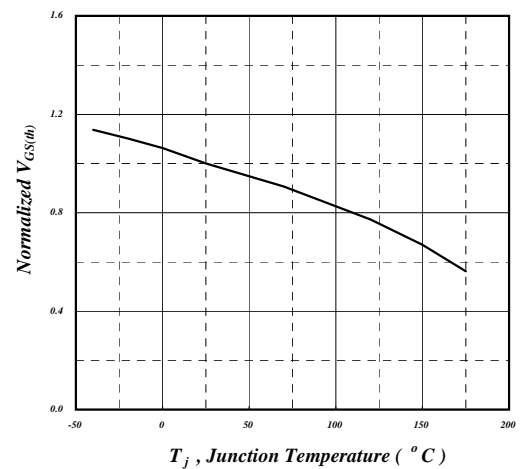
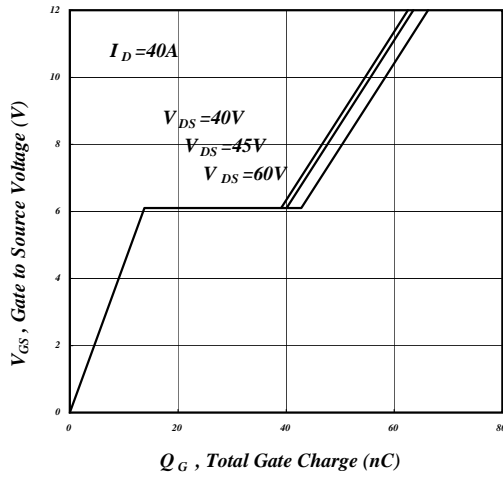
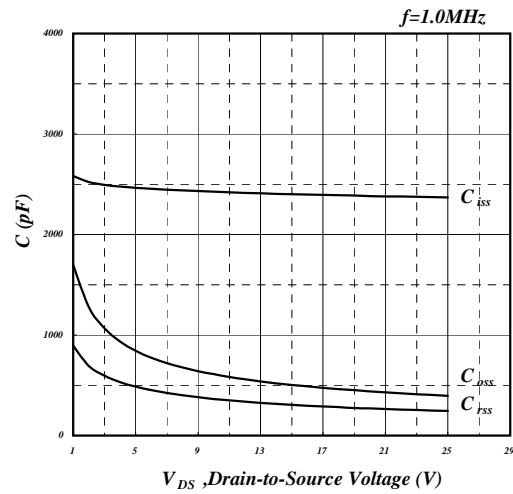
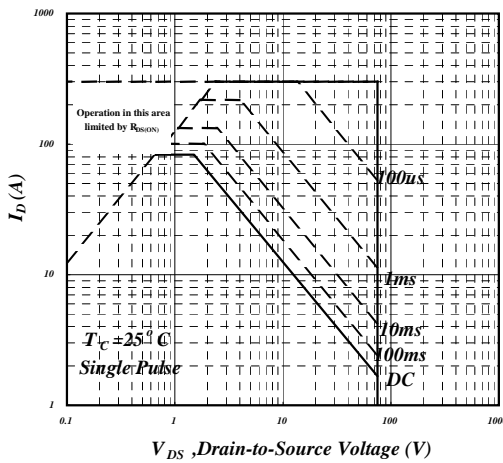
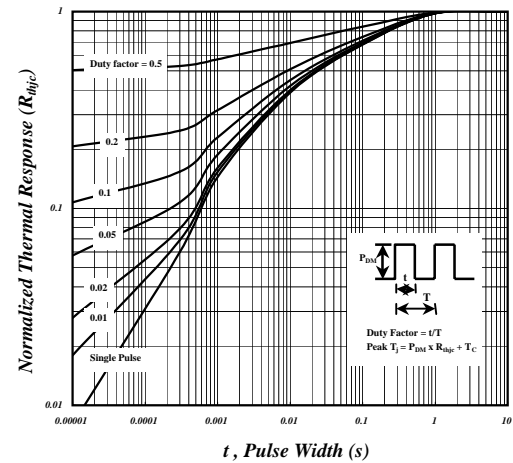
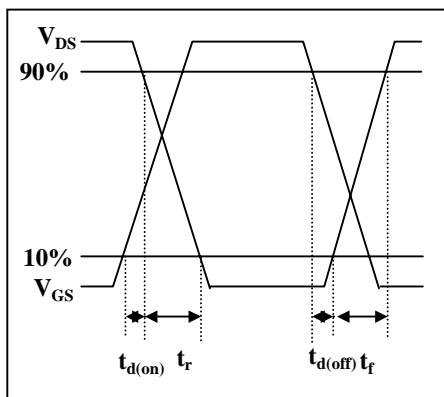
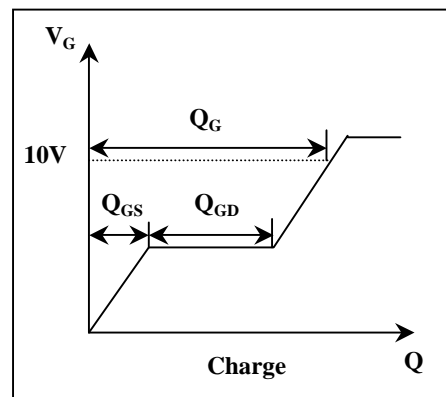
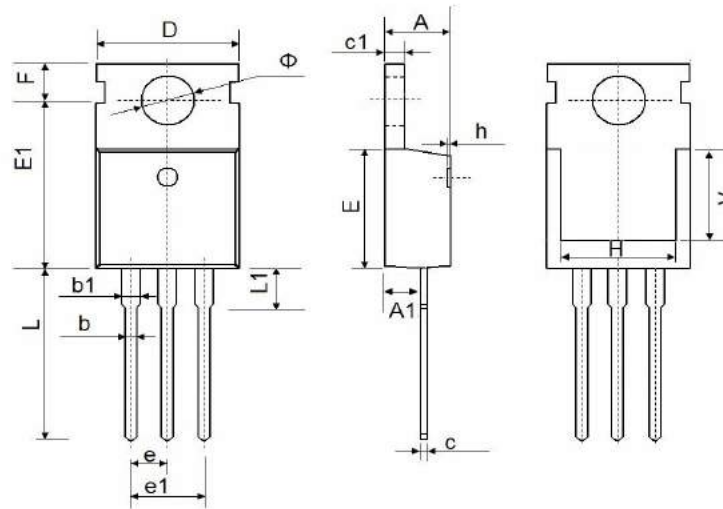
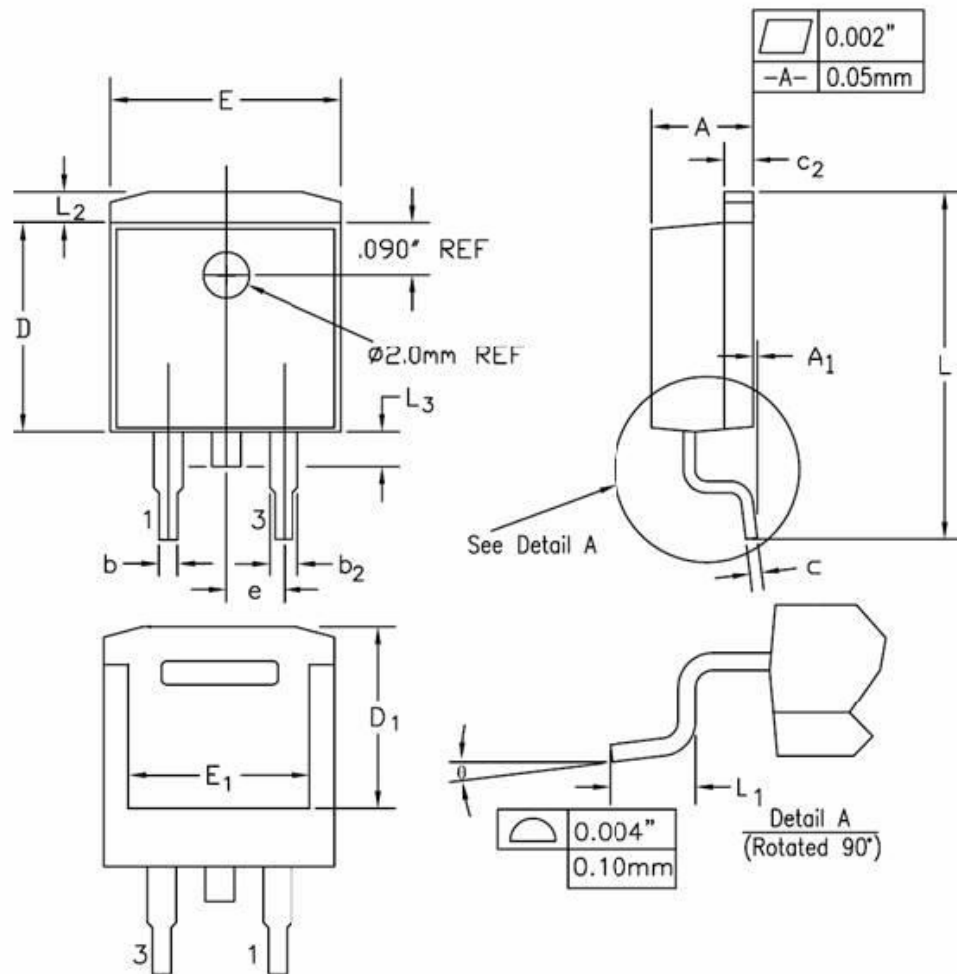


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

80V N-Channel Enhancement Mode MOSFET

Fig 7. Gate Charge Characteristics

Fig 8. Typical Capacitance Characteristics

Fig 9. Maximum Safe Operating Area

Fig 10. Effective Transient Thermal Impedance

Fig 11. Switching Time Waveform

Fig 12. Gate Charge Waveform

80V N-Channel Enhancement Mode MOSFET
TO-220-3L Package Information


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181
A1	2.250	2.550	0.089	0.100
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.330	0.650	0.013	0.026
c1	1.200	1.400	0.047	0.055
D	9.910	10.250	0.390	0.404
E	8.9500	9.750	0.352	0.384
E1	12.650	12.950	0.498	0.510
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
F	2.650	2.950	0.104	0.116
H	7.900	8.100	0.311	0.319
h	0.000	0.300	0.000	0.012
L	12.900	13.400	0.508	0.528
L1	2.850	3.250	0.112	0.128
V	7.500 REF.		0.295 REF.	
φ	3.400	3.800	0.134	0.150

80V N-Channel Enhancement Mode MOSFET
TO-263-2L Package Information


SYMBOL	INCHES		MILLIMETERS		NOTES
	MIN	MAX	MIN	MAX	
A	0.170	0.180	4.32	4.57	
A1	-	0.010	-	0.25	
b	0.028	0.037	0.71	0.94	
b2	0.045	0.055	1.15	1.40	
c	0.018	0.024	0.46	0.61	
c2	0.048	0.055	1.22	1.40	
D	0.350	0.370	8.89	9.40	
D1	0.315	0.324	8.01	8.23	
E	0.395	0.405	10.04	10.28	
E1	0.310	0.318	7.88	8.08	
e	0.100 BSC.		2.54 BSC.		
L	0.580	0.620	14.73	15.75	
L1	0.090	0.110	2.29	2.79	
L2	0.045	0.055	1.15	1.39	
L3	0.050	0.070	1.27	1.77	
θ	0°	8°	0°	8°	