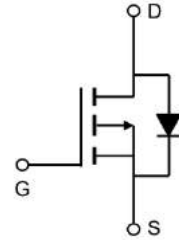


200V P-Channel Enhancement Mode MOSFET

Description

The 13P20 is silicon P-channel Enhanced VDMOSFETs, is obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency.



General Features

$V_{DS} = -200V, I_D = -13A$

$R_{DS(ON)} < 0.42\Omega @ V_{GS} = 10V$

Application

Power amplifier

motor drive




Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

| Symbol | Parameter | Rating | Units |
|-------------------------|--|-------------|--------------|
| V_{DS} | Drain-Source Voltage | -200 | V |
| V_{GS} | Gate-Source Voltage | ± 20 | V |
| $I_D T_C = 25^\circ C$ | Continuous Drain Current | -13 | A |
| $I_D T_C = 100^\circ C$ | Continuous Drain Current | -7.2 | A |
| IDM | Pulsed Drain Current ^a | -52 | A |
| EAS | Single Pulse Avalanche Energy ^b | 750 | mJ |
| IAR | Repetitive Avalanche Current ^a | -11 | A |
| EAR | Repetitive Avalanche Energy ^a | 13 | mJ |
| $P_D T_C = 25^\circ C$ | Maximum Power Dissipation | 125 | W |
| dV/dt | Peak Diode Recovery dV/dt ^c | -5.0 | V/ns |
| T_J, T_{stg} | Operating Junction and Storage Temperature Range | -55 to +150 | $^\circ C$ |
| RthJA | Maximum Junction-to-Ambient | 62 | $^\circ C/W$ |
| RthCS | Case-to-Sink, Flat, Greased Surface | 0.50 | $^\circ C/W$ |
| RthJC | Maximum Junction-to-Case (Drain) | 1.0 | $^\circ C/W$ |

200V P-Channel Enhancement Mode MOSFET

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit | |
|----------------------|---|---|------|------|---|---------------------------|-----|
| V_{DS} | Drain-Source Breakdown Voltage | $V_{GS} = 0\text{ V}, I_D = -250\ \mu\text{A}$ | -200 | - | - | V | |
| $\square V_{DS}/T_J$ | V_{DS} Temperature Coefficient | Reference to 25°C , $I_D = -1\ \text{mA}$ | - | -0.2 | - | $\text{V}/^\circ\text{C}$ | |
| $V_{GS(th)}$ | Gate-Source Threshold Voltage | $V_{DS} = V_{GS}, I_D = -250\ \mu\text{A}$ | -2.0 | - | -4.0 | V | |
| I_{GSS} | Gate-Source Leakage | $V_{GS} = \pm 20\ \text{V}$ | - | - | ± 100 | nA | |
| I_{DSS} | Zero Gate Voltage Drain Current | $V_{DS} = -200\ \text{V}, V_{GS} = 0\ \text{V}$ | - | - | -100 | μA | |
| I_{DSS} | | $V_{DS} = -160\ \text{V}, V_{GS} = 0\ \text{V}, T_J = 125^\circ\text{C}$ | - | - | -500 | | |
| $R_{DS(on)}$ | Drain-Source On-State Resistance | $V_{GS} = -10\ \text{V}, I_D = -5.5\ \text{A}^b$ | - | 0.34 | 0.42 | Ω | |
| g_{fs} | Forward Transconductance | $V_{DS} = -50\ \text{V}, I_D = -6.6\ \text{A}^b$ | 4.1 | - | - | S | |
| C_{iss} | Input Capacitance | $V_{GS} = 0\ \text{V},$ $V_{DS} = -25\ \text{V}, f = 1.0$ MHz, see fig. 5 | - | 1200 | - | μF | |
| C_{oss} | Output Capacitance | | - | 370 | - | | |
| C_{rss} | Reverse Transfer Capacitance | | - | 81 | - | | |
| Q_g | Total Gate Charge | $V_{GS} = -10\ \text{V}$ | - | - | 44 | nC | |
| Q_{gs} | Gate-Source Charge | | | | $I_D = -11\ \text{A}, V_{DS} = -160\ \text{V}$, see fig. 6 and 13 ^b | | 7.1 |
| Q_{gd} | Gate-Drain Charge | | | | 27 | | |
| $t_{d(on)}$ | Turn-On Delay Time | $V_{DD} = -100\ \text{V}, I_D = -11\ \text{A}$ $R_g = 9.1\ \Omega, R_D = 8.6\ \Omega$, see fig. 10 ^b | - | 14 | - | ns | |
| t_r | Rise Time | | - | 43 | - | | |
| $t_{d(off)}$ | Turn-Off Delay Time | | - | 39 | - | | |
| t_f | Fall Time | | - | 38 | - | | |
| R_g | Gate Input Resistance | $f = 1\ \text{MHz}$, open drain | 0.3 | - | 1.7 | Ω | |
| I_S | Continuous Source-Drain Diode Current | Between lead, 6 mm (0.25") from package and center of die contact | - | - | -11 | A | |
| I_{SM} | Pulsed Diode Forward Current ^a |  | - | - | -44 | | |
| V_{SD} | Body Diode Voltage | $T_J = 25^\circ\text{C}, I_S = -11\ \text{A}, V_{GS} = 0\ \text{V}^b$ | - | - | -5 | V | |
| t_{rr} | Body Diode Reverse Recovery Time | $T_J = 25^\circ\text{C}, I_F = -11\ \text{A}, dI/dt = 100\ \text{A}/\mu\text{s}^b$ | - | 250 | 300 | ns | |
| Q_{rr} | Body Diode Reverse Recovery Charge | | - | 2.9 | 3.6 | μC | |
| t_{on} | Forward Turn-On Time | Intrinsic turn-on time is negligible (turn-on is dominated by L_S and L_D) | | | | | |

Notes

- Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- Pulse width $\leq 300\ \mu\text{s}$; duty cycle $\leq 2\ \%$.

Typical Characteristics

200V P-Channel Enhancement Mode MOSFET

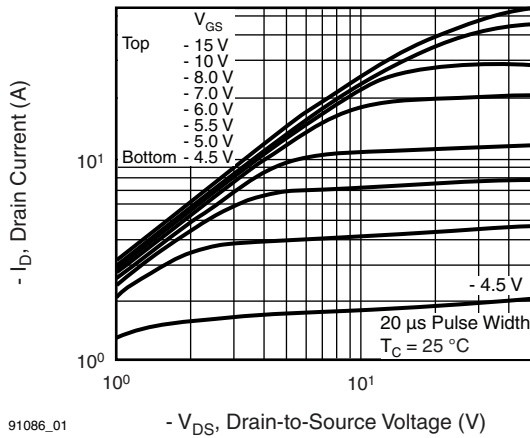


Fig. 1 - Typical Output Characteristics, $T_C = 25\text{ }^\circ\text{C}$

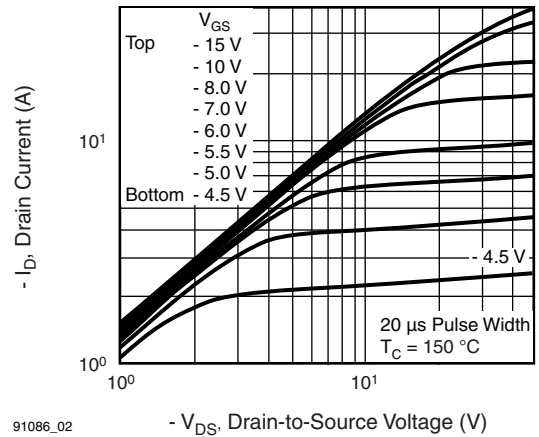


Fig. 2 - Typical Output Characteristics, $T_C = 150\text{ }^\circ\text{C}$

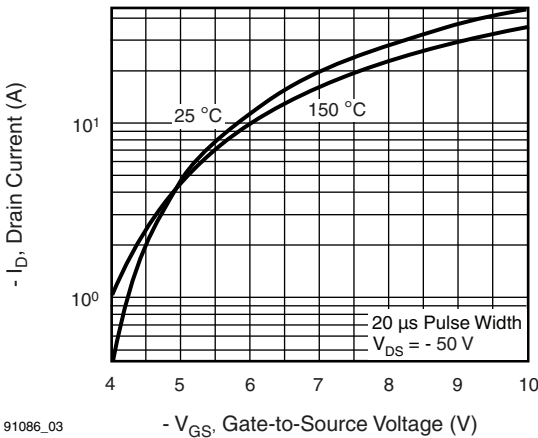


Fig. 3 - Typical Transfer Characteristics

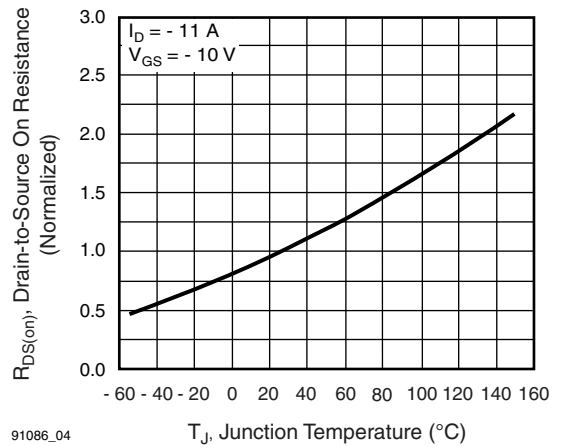


Fig. 4 - Normalized On-Resistance vs. Temperature

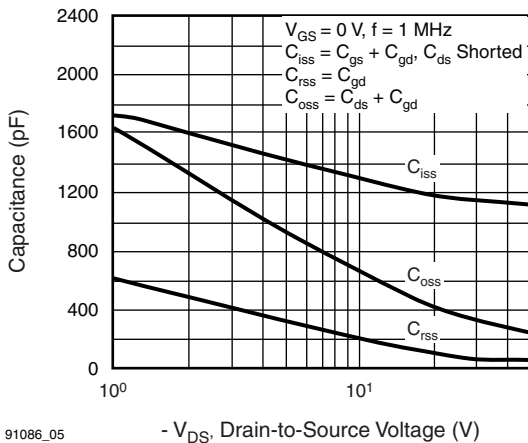


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

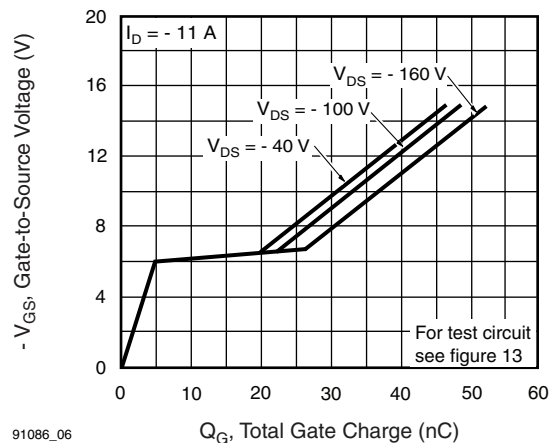


Fig. 6 - Typical Gate Charge vs. Drain-to-Source Voltage

200V P-Channel Enhancement Mode MOSFET

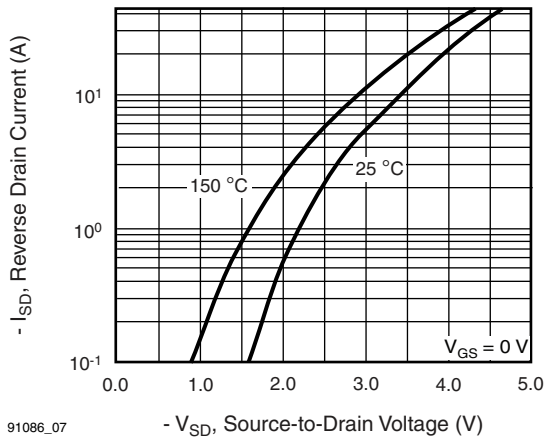


Fig. 7 - Typical Source-Drain Diode Forward Voltage

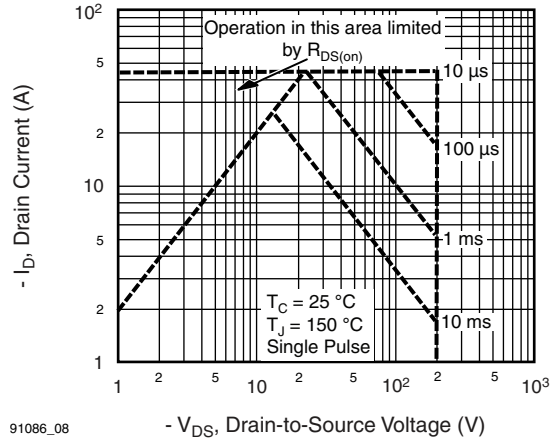


Fig. 8 - Maximum Safe Operating Area

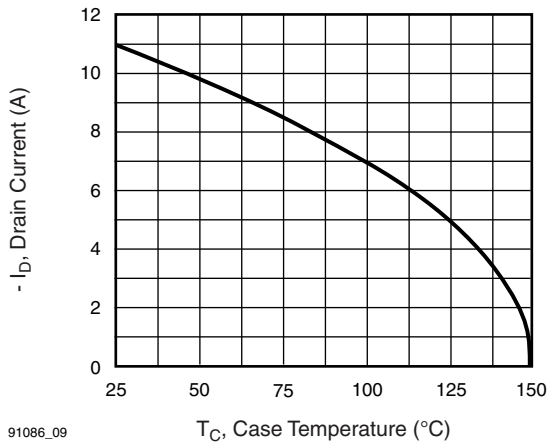


Fig. 9 - Maximum Drain Current vs. Case Temperature

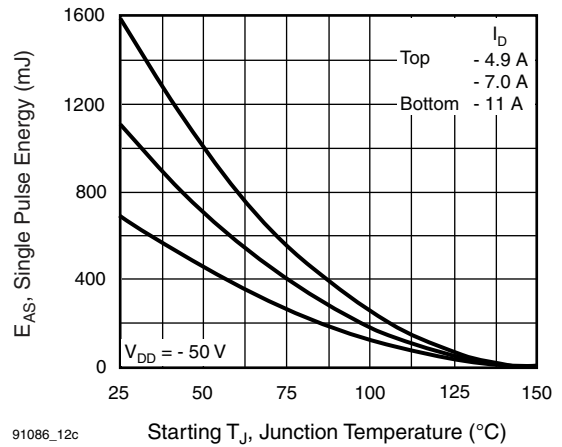


Fig. 10 - Maximum Avalanche Energy vs. Drain Current

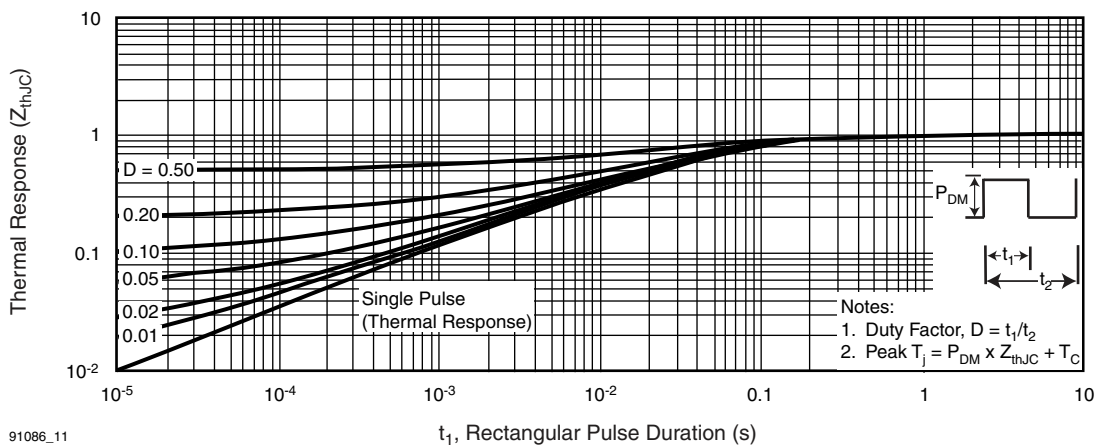


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

200V P-Channel Enhancement Mode MOSFET
